



Departmental Activity Report

Department: Electrical and Electronics Engineering (EEE)

Activity Category: Field Visit / Educational Visit

Title of the Activity: Visit to SemiconIndia 2026

Theme/Focus Area: Industry-Academia Integration in Semiconductor Manufacturing

1. Basic Details

- Date of Activity: 17th August 2026
- Day: Monday
- Time: 11:00 AM onwards
- Venue/Platform: Yashobhoomi (India International Convention and Expo Centre – IICC), Dwarka, New Delhi
- Organized by: ElectroTech Technical Society, Department of Electrical and Electronics Engineering (EEE), MAIT
- Activity Coordinator(s): Dr. Poonam Juneja
- Number of Participants:
 - Students: 30 (5th Semester, B.Tech EEE)
 - Faculty: Dr. Poonam Juneja (Coordinator)
 - External Participants: Not applicable

2. Resource Person / Speaker Details

Not applicable - the activity was an industrial/educational exhibition visit and did not involve a designated resource person or invited speaker.

3. Objectives of the Activity

- To provide students with practical, first-hand exposure to the semiconductor industry's manufacturing ecosystem, fabrication technologies, and supply chain logistics.
- To bridge the gap between classroom learning and industrial practice in areas such as VLSI design, chip fabrication, metrology, and advanced packaging.
- To expose students to leading global and domestic semiconductor companies and to India's evolving semiconductor manufacturing corridors.
- To create awareness among students about emerging career pathways and skill requirements within the semiconductor sector.
- To foster an innovation driven mindset through interaction with industry professionals and deep-tech startups.

4. Description of the Activity

ElectroTech, the technical society of the EEE Department, organized an educational visit to SemiconIndia 2026, held at Yashobhoomi (IICC), Dwarka, New Delhi, for more than 15 students of the 5th semester, coordinated by Dr. Poonam Juneja. The visit offered students hands on exposure to the semiconductor industry's manufacturing ecosystem and global supply chain. Students explored exhibitor booths covering wafer fabrication, specialty chemicals, precision metrology equipment, cleanroom automation, chip design services, and advanced packaging, engaging with companies such as SanDisk, Infosys, Delta Electronics, Hitachi, Mitutoyo, Daifuku, Fujifilm, Tata Electronics, HCL Tech, and L&T, as well as international pavilions from Japan, South Korea, Singapore, and Malaysia. Through direct booth interactions and industry conversations, students gained insight into equipment handling, the material science behind chip fabrication, and emerging career opportunities, while also receiving promotional souvenirs from several exhibitor stalls, adding an enjoyable element to the learning experience.

5. Learning Outcomes / Impact

- Skills gained: Practical understanding of semiconductor fabrication, metrology, cleanroom automation, and chip packaging processes.
- Knowledge enhancement: Direct linkage of academic subjects (control systems, power electronics, VLSI design, instrumentation) to real world industrial applications.
- Awareness created: Exposure to India's semiconductor manufacturing corridors, national policy initiatives, and the global semiconductor value chain.
- Innovation mindset development: Interaction with deep tech startups and industry professionals inspired an innovation and career oriented outlook among students.

6. Feedback Summary

- Mode of feedback collection: Verbal discussion
- Average rating: 4/5
- Key feedback insights: Students reported the visit to be highly informative and inspiring, strengthening their understanding of semiconductor manufacturing and career pathways.

7. Challenges Faced

- High security protocols at the venue led to minor delays and hassle while entering the exhibition hall.
- Poor network connectivity inside the hall made it difficult for students and the coordinator to stay in touch with each other, posing a challenge in overall coordination.

8. Recommendations / Future Scope

- Establish a dedicated Semiconductor Focus Group within ElectroTech to track advancements in EDA tools, packaging, and lithography.
- Integrate commercial EDA tool training and cleanroom protocol workshops into the EEE curriculum.
- Pursue collaborative industry internships and capstone projects with companies engaged during the visit.
- Maintain continuous engagement with MeitY and India Semiconductor Mission (ISM) workforce development initiatives.

9. Annexures

- Annexure I – Attendance Sheet
- Annexure II – Event Poster/Brochure
- Annexure III – Participant Feedback Summary

10. Photographs



GROUP PHOTO AT THE EVENT



PHOTO AT DOU YEE LMTD



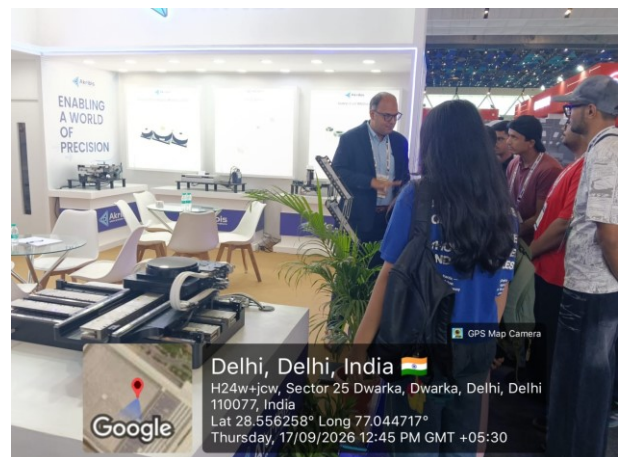
PHOTO AT BESI



DISPLAY AT GUJARAT PAVILION



PHOTO AT JT CORP



STUDENTS INTERACTING AT STALLS

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